

U.S. PRO
J1010/038206
01/02/02
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Atty. Docket: POU920010165US1
Marvin J. RICH et al. : APPLICATIONS BRANCH
Serial No. (not yet assigned) :
Filed: Herewith :
For: **DELAY CORRELATION ANALYSIS AND REPRESENTATION
FOR VITAL COMPLIANT VHDL MODELS**

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

SIR:

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that the information provided is prior art.

This IDS is submitted under:

37 CFR 1.97(b) - No Fee.
 37 CFR 1.97(c) - No Fee, with Certification.
 37 CFR 1.97(c) - Fee.
 37 CFR 1.97(d) - Fee, Certification & Petition.

The Commissioner is authorized to charge any required fees under 37 CFR 1.17(p) and (i) (1) to Deposit Account No. 09-0463.

Date: 1/2/02

Respectfully submitted,
By: Jose Gutman
Jose Gutman
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